

## PATENT ABSTRACTS OF JAPAN

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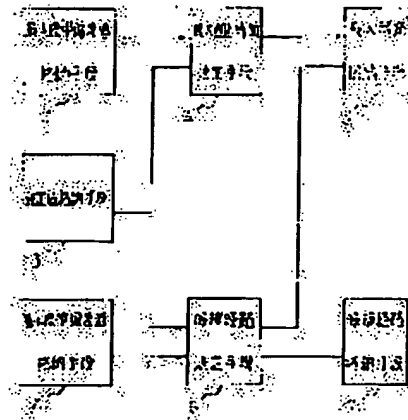
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## (54) CIRCUIT PROCESSING DEVICE

## (57)Abstract:

**PURPOSE:** To prevent the drop-out of a route possible to be a maximum route value in the case to go from a beginning point to an end point through a remarked point by determining the candidate of the route from the beginning point to the remarked point possible to be the maximum route value by using a minimum standard deviation value storing means, a determinate value storing means and a maximum route storing means.

**CONSTITUTION:** In the delay analysis of a logical circuit, a candidate route determining means 6 determines the candidate of the route from the beginning point to the remarked point which is possible to be the maximum route value, i.e., a maximum delay time in the case to go from the beginning point of the end point through the remarked point by using the minimum standard deviation value storing means 2, the determinate value storing means 3 and the maximum route storing means 5. Then, a candidate route storing means 7 stores the candidate of the route possible to be the maximum delay time in the case to go from the beginning point to the end point through the remarked point. Thus, the route possible to be the maximum route value in the case to go from the beginning point to the end point through the remarked point can be stored without drop-out.



## LEGAL STATUS

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